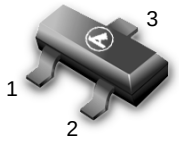
General Purpose Transistor

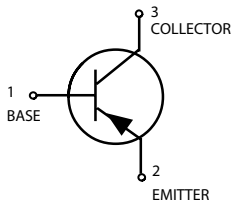
PNP Silicon

LMBT2907LT1G
LMBT2907ALT1G
S-LMBT2907LT1G
S-LMBT2907ALT1G

- We declare that the material of product compliance with RoHS requirements.
- S- Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.



SOT–23



ORDERING INFORMATION

Device	Marking	Shipping
LMBT2907LT1G,S-LMBT2907LT1G	M2B	3000/Tape & Reel
LMBT2907LT3G,S-LMBT2907LT3G	M2B	10000/Tape & Reel
LMBT2907ALT1G,S-LMBT2907ALT1G	2F	3000/Tape & Reel
LMBT2907ALT3G,S-LMBT2907ALT3G	2F	10000/Tape & Reel

MAXIMUM RATINGS

Rating	Symbol	Value 2907	Value 2907A	Unit
Collector–Emitter Voltage	V_{CEO}	–40	–60	Vdc
Collector–Base Voltage	V_{CBO}	–60		Vdc
Emitter–Base Voltage	V_{EBO}	–5.0		Vdc
Collector Current — Continuous	I_C	–600		mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR– 5 Board, (1) T_A = 25°C	P_D	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (2) T_A = 25°C	P_D	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T_J , T_{stg}	–55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR–5 = 1.0 × 0.75 × 0.062 in.
2. Alumina = 0.4 × 0.3 × 0.024 in. 99.5% alumina.

LMBT2907LT1G LMBT2907ALT1G
S-LMBT2907LT1G S-LMBT2907ALT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage(3)		$V_{(BR)CEO}$			Vdc
(I_C = –10 mAdc, I_B = 0)	LMBT2907		–40	—	
	LMBT2907A		–60	—	
Collector–Emitter Breakdown Voltage(I_C = –10 µAdc, I_E = 0)		$V_{(BR)CBO}$	–60	—	Vdc
Emitter–Base Breakdown Voltage(I_E = –10 µAdc, I_C = 0)		$V_{(BR)EBO}$	–5.0	—	Vdc
Collector Cutoff Current(V_{CB} = –30Vdc, $I_{BE(OFF)}$ = –0.5Vdc)		I_{CEX}	—	–50	nAdc
Collector Cutoff Current		I_{CBO}			µAdc
(V_{CB} = –50Vdc, I_E = 0)	LMBT2907		—	–0.020	
	LMBT2907A		—	–0.010	
(V_{CB} = –50Vdc, I_E = 0, T_A =125°C)	LMBT2907		—	–20	
	LMBT2907A		—	–10	
Base Current(V_{CE} = –30Vdc, $V_{EB(off)}$ = –0.5Vdc)		I_B	—	–50	nAdc
ON CHARACTERISTICS					
DC Current Gain		h_{FE}			
(I_C = –0.1mAdc, V_{CE} = –10 Vdc)	LMBT2907		35	—	
	LMBT2907A		75	—	
(I_C =–1.0mAdc, V_{CE} = –10 Vdc)	LMBT2907		50	—	
	LMBT2907A		100	—	
(I_C = –10 mAdc, V_{CE} = –10Vdc)	LMBT2907		75	—	
	LMBT2907A		100	—	
(I_C = –150mAdc, V_{CE} =–10 Vdc)(3)	LMBT2907		—	—	
	LMBT2907A		100	300	
(I_C = –500mAdc, V_{CE} =–10 Vdc)(3)	LMBT2907		30	—	
	LMBT2907A		50	—	
Collector–Emitter Saturation Voltage(3)		$V_{CE(sat)}$			Vdc
(I_C = –150mAdc, I_B = –15 mAdc)			—	–0.4	
(I_C = –500 mAdc, I_B = –50 mAdc)			—	–1.6	
Base–Emitter Saturation Voltage(3)		$V_{BE(sat)}$			Vdc
(I_C = –150mAdc, I_B = –15 mAdc)			—	–1.3	
(I_C = –500mAdc, I_B = –50 mAdc)			—	–2.6	

3. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.

LMBT2907LT1G LMBT2907ALT1G
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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
SMALL–SIGNAL CHARACTERISTICS					
Current–Gain — Bandwidth Product(3),(4) (I_C = –50mAdc, V_{CE}= –20Vdc, f = 100MHz)		f_T	200	—	MHz
Output Capacitance (V_{CB} = –10 Vdc, I_E = 0, f = 1.0 MHz)		C_{obo}	—	8.0	pF
Input Capacitance (V_{EB} = –2.0Vdc, I_C = 0, f = 1.0 MHz)		C_{ibo}	—	30	pF
SWITCHING CHARACTERISTICS					
Turn–On Time	(V_{CC} = –30 Vdc, I_C = –150 mAdc, I_{B1} = –15 mAdc)	t_{on}	—	45	ns
Delay Time		t_d	—	10	
Rise Time		t_r	—	40	
Fall Time	(V_{CC} = –6.0 Vdc, I_C = –150 mAdc,I_{B1} = I_{B2} = 15 mAdc)	t_f	—	60	ns
Storage Time		t_s	—	225	
Turn–Off Time		t_{off}	—	280	

4. f_T is defined as the frequency at which $|h f_e|$ extrapolates to unity.

TYPICAL CHARACTERISTICS

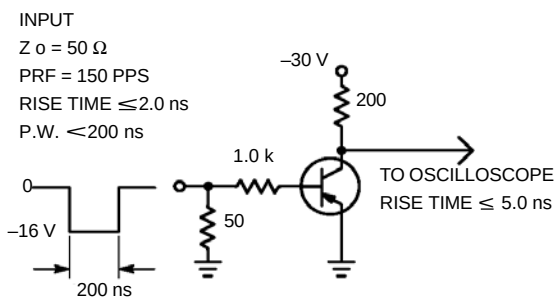

Figure 1. Delay and Rise Time Test Circuit

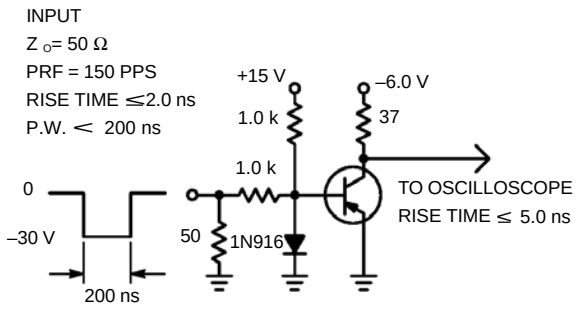

Figure 2. Storage and Fall Time Test Circuit

TYPICAL CHARACTERISTICS

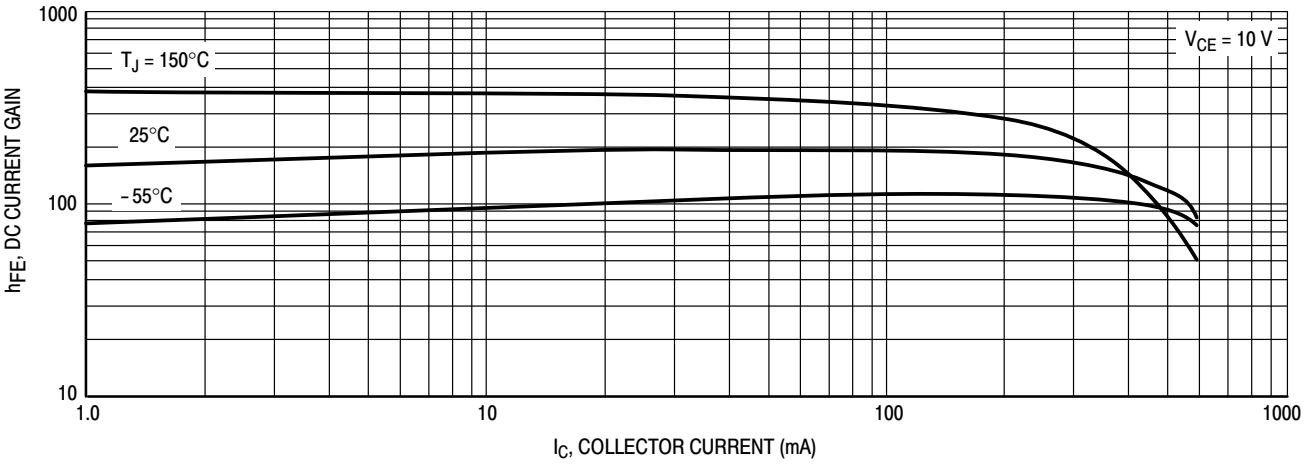

Figure 3. DC Current Gain

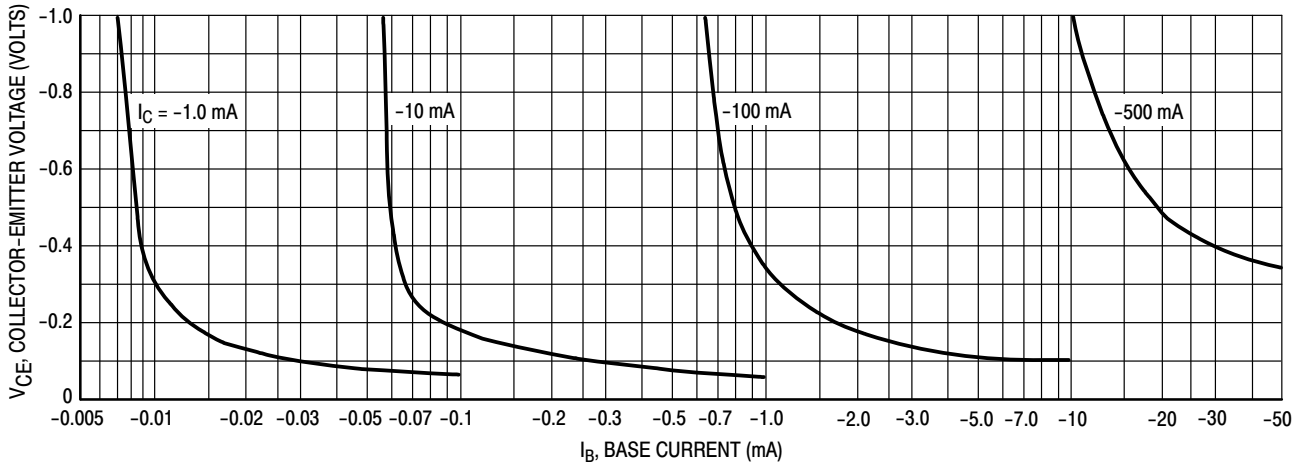

Figure 4. Collector Saturation Region

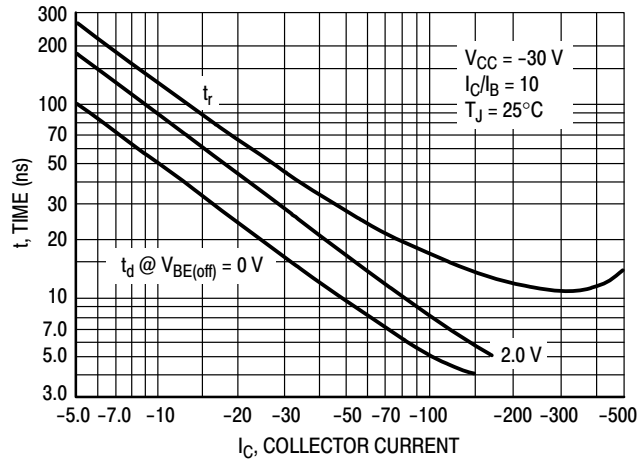

Figure 5. Turn–On Time

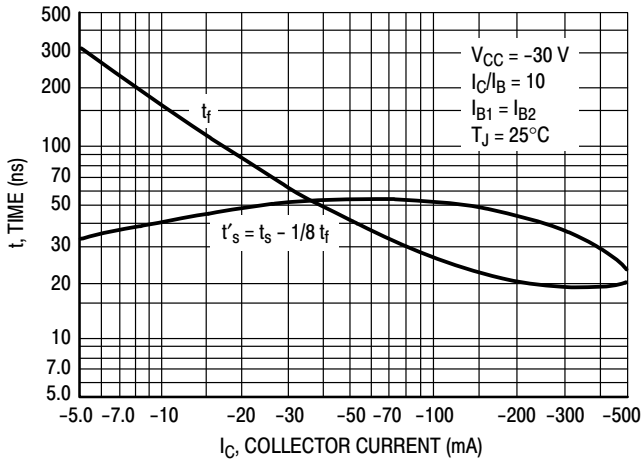

Figure 6. Turn–Off Time

TYPICAL SMALL–SIGNAL CHARACTERISTICS

NOISE FIGURE

V_{CE} = 10 Vdc, T_A = 25°C

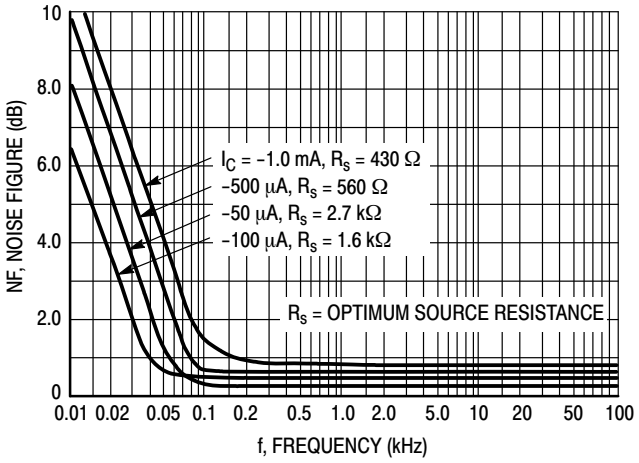

Figure 7. Frequency Effects

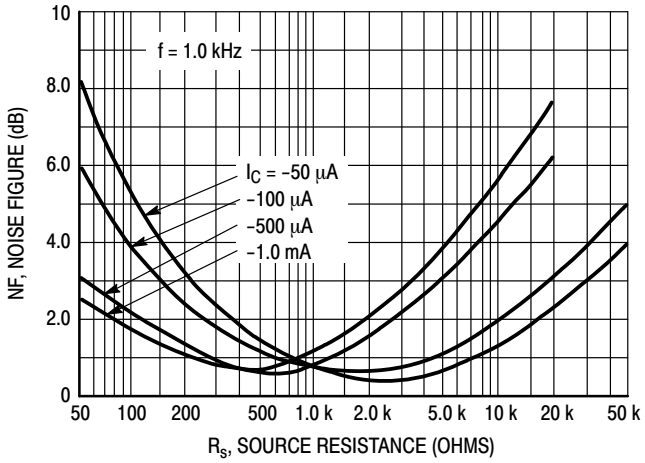

Figure 8. Source Resistance Effects

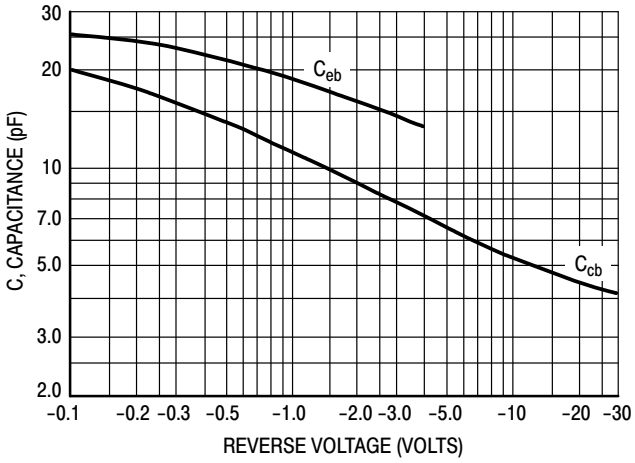

Figure 9. Capacitances

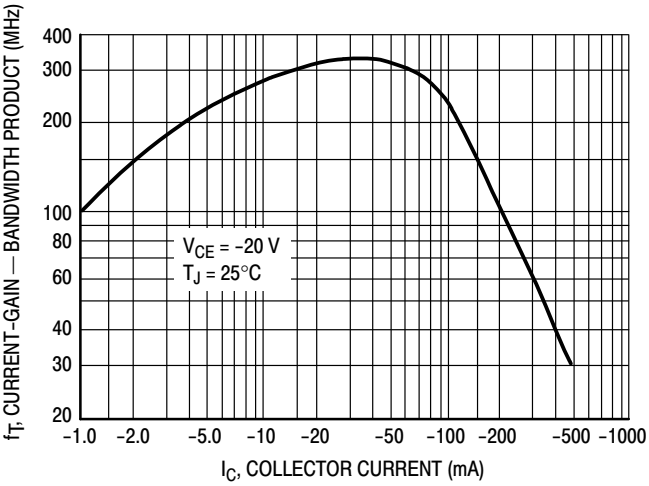

Figure 10. Current–Gain – Bandwidth Product

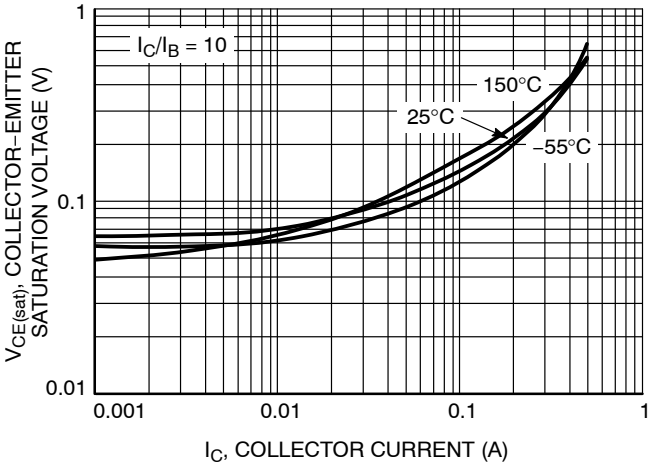

Figure 11. Collector Emitter Saturation Voltage vs. Collector Current

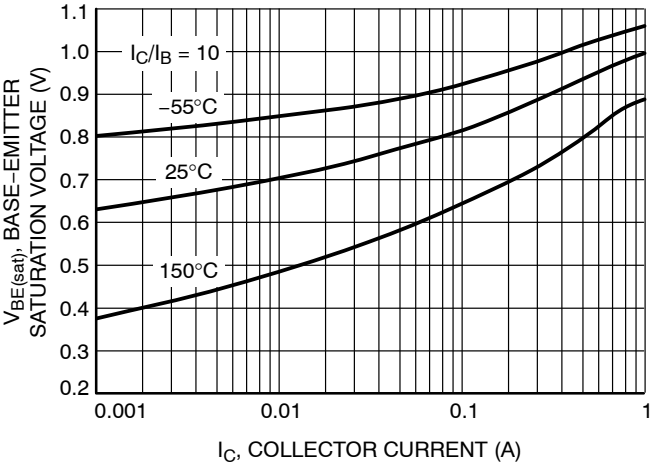

Figure 12. Base Emitter Saturation Voltage vs. Collector Current

TYPICAL SMALL–SIGNAL Characteristics

NOISE FIGURE

V_{CE} = 10 Vdc, T_A = 25°C

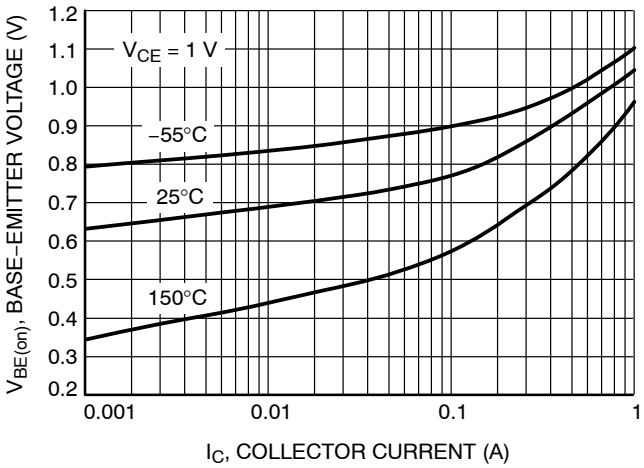

Figure 13. Base Emitter Voltage vs. Collector Current

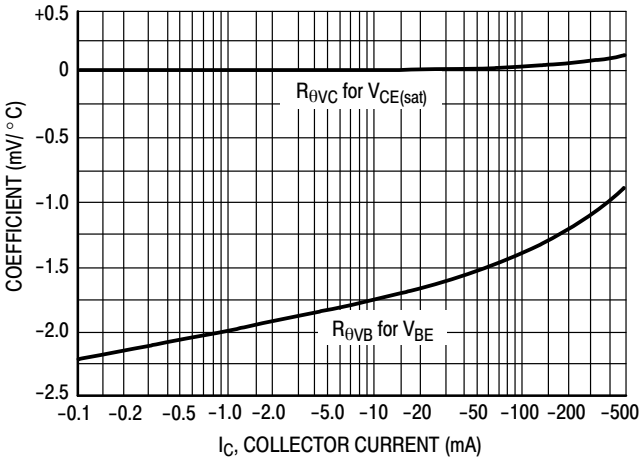

Figure 14. Temperature Coefficients

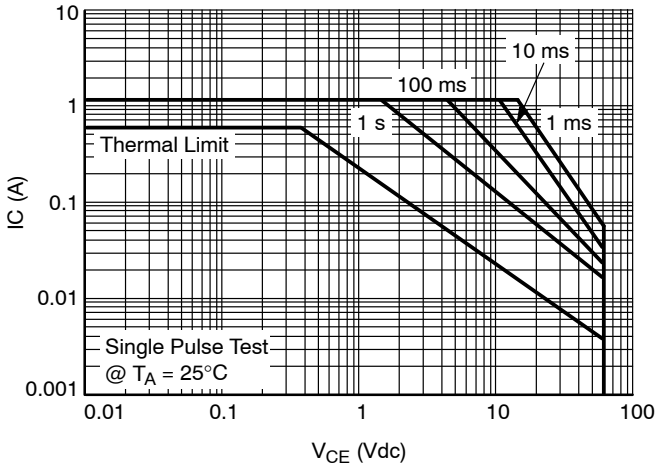

Figure 15. Safe Operating Area

LMBT2907LT1G LMBT2907ALT1G
S-LMBT2907LT1G S-LMBT2907ALT1G

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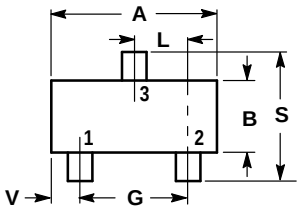

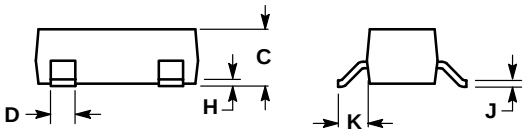

NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

PIN 1. BASE
2. EMITTER
3. COLLECTOR