



PBSS4140T

40 V, 1 A NPN low V_{CEsat} (BISS) transistor

1 April 2023

Product data sheet

1. General description

NPN low V_{CEsat} transistor in a small SOT23 plastic package. PNP complement: PBSS5140T.

2. Features and benefits

- Low collector-emitter saturation voltage
- High current capabilities
- Improved device reliability due to reduced heat generation

3. Applications

- General purpose switching and muting
- LCD backlighting
- Supply line switching circuits
- Battery driven equipment (mobile phones, video cameras and hand-held devices).

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	2	A
R _{CEsat}	collector-emitter saturation resistance	I _C = 500 mA; I _B = 50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	260	500	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SOT23	 sym123
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4140T	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS4140T	ZT%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	40	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	2	A
I _{BM}	peak base current			-	1	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
			[2]	-	450	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	417	K/W
			[2]	-	-	278	K/W

- [1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 1 cm².

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100\ \mu\text{A}$; $I_E = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	40	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2\ \text{mA}$; $I_B = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	40	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage (collector open)	$I_E = 100\ \mu\text{A}$; $I_C = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	5	-	-	V
I_{CBO}	collector-base cut-off current	$V_{CB} = 40\ \text{V}$; $I_E = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	100	nA
		$V_{CB} = 40\ \text{V}$; $I_E = 0\ \text{A}$; $T_{\text{amb}} = 150\ ^\circ\text{C}$	-	-	50	μA
I_{CEO}	collector-emitter cut-off current (base open)	$I_B = 0\ \text{A}$; $V_{CE} = 30\ \text{V}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	100	nA
I_{EBO}	emitter-base cut-off current	$V_{EB} = 5\ \text{V}$; $I_C = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	100	nA
I_{CES}	collector-emitter cut-off current	$V_{CE} = 30\ \text{V}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	100	nA
h_{FE}	DC current gain	$V_{CE} = 5\ \text{V}$; $I_C = 1\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	300	-	-	
		$V_{CE} = 5\ \text{V}$; $I_C = 500\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	300	-	900	
		$V_{CE} = 5\ \text{V}$; $I_C = 1\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	200	-	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 100\ \text{mA}$; $I_B = 1\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	200	mV
		$I_C = 500\ \text{mA}$; $I_B = 50\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	250	mV
		$I_C = 1\ \text{A}$; $I_B = 100\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	500	mV
R_{CEsat}	collector-emitter saturation resistance	$I_C = 500\ \text{mA}$; $I_B = 50\ \text{mA}$; pulsed; $t_p \leq 300\ \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	260	500	m Ω
V_{BEsat}	base-emitter saturation voltage	$I_C = 1\ \text{A}$; $I_B = 100\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	1.2	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = 5\ \text{V}$; $I_C = 1\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	1.1	V
f_T	transition frequency	$V_{CE} = 10\ \text{V}$; $I_C = 50\ \text{mA}$; $f = 100\ \text{MHz}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	150	-	-	MHz
C_c	collector capacitance	$V_{CB} = 10\ \text{V}$; $I_E = 0\ \text{A}$; $i_e = 0\ \text{A}$; $f = 1\ \text{MHz}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	10	pF

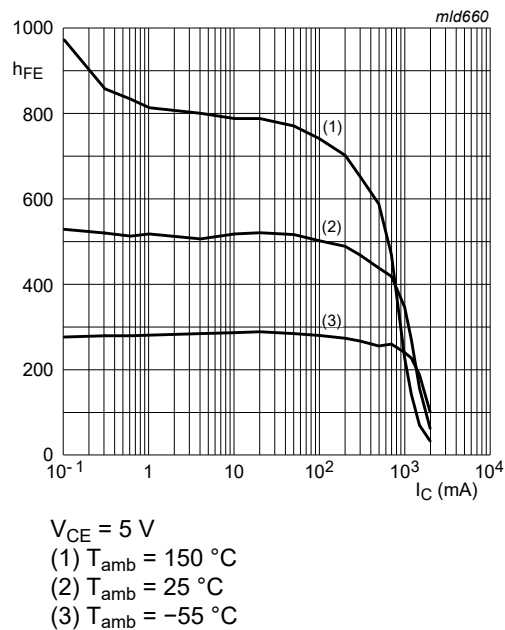


Fig. 1. DC current gain as a function of collector current; typical values

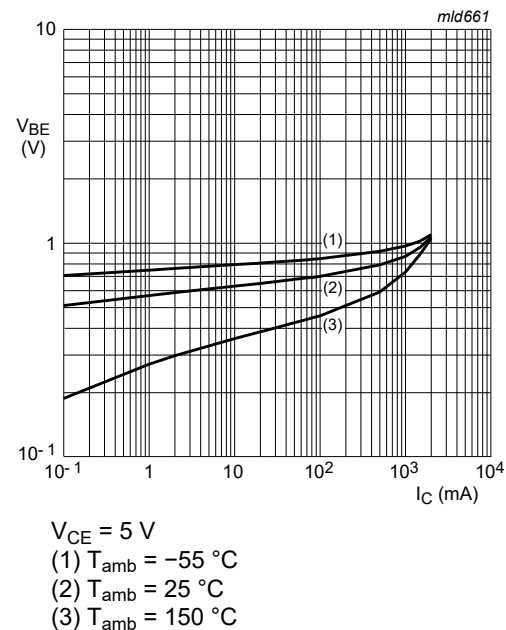


Fig. 2. Base-emitter voltage as a function of collector current; typical values

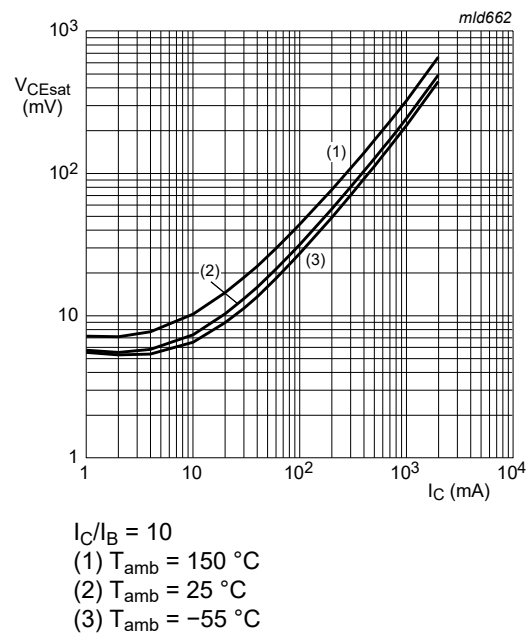


Fig. 3. Collector-emitter saturation voltage as a function of collector current; typical values

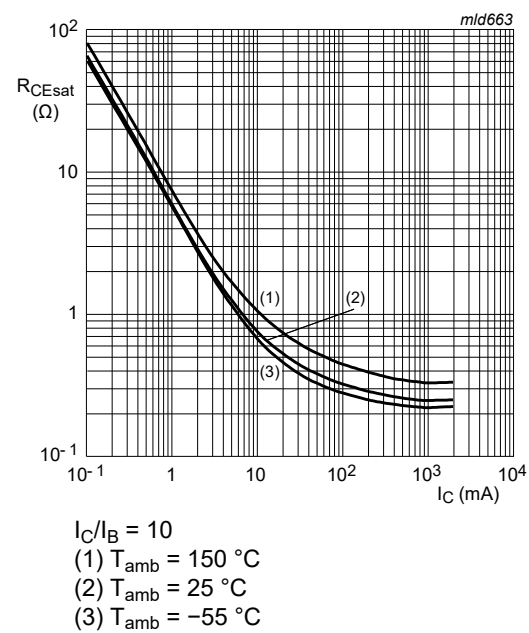
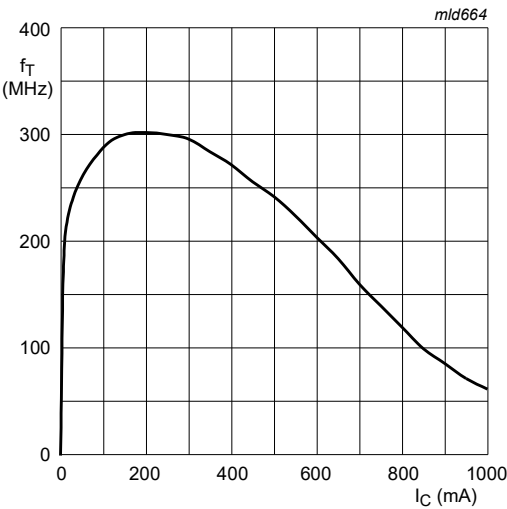


Fig. 4. Equivalent on-resistance as a function of collector current; typical values



$V_{CE} = 10\text{ V}$

Fig. 5. Transition frequency as a function of collector current

11. Package outline

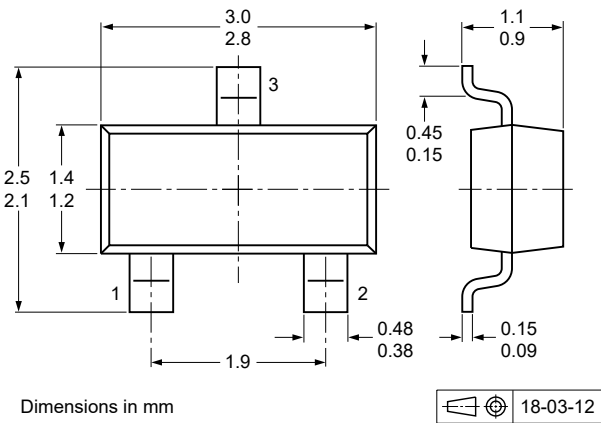


Fig. 6. Package outline SOT23

12. Soldering

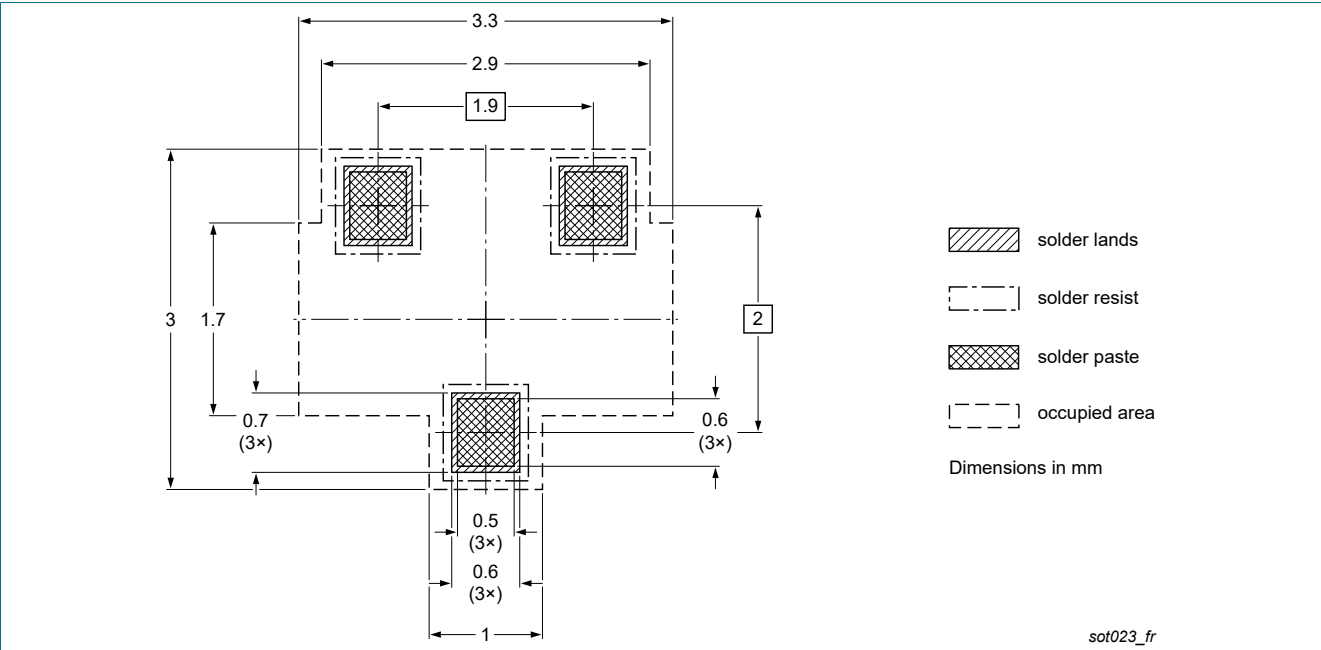


Fig. 7. Reflow soldering footprint for SOT23

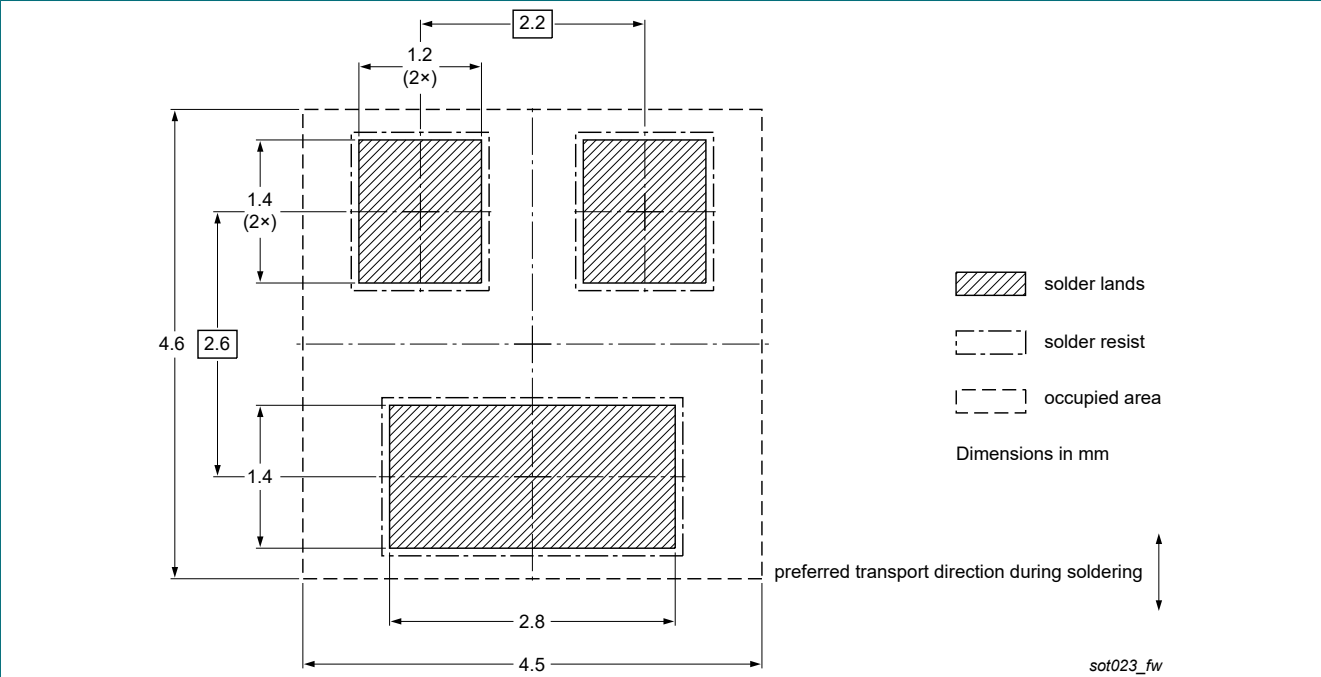


Fig. 8. Wave soldering footprint for SOT23

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4140T v.3	20230401	Product data sheet	-	PBSS4140T v.2
Modifications:	- The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. - Legal texts have been adapted to the new company name where appropriate. - Product changed to non automotive. Please refer to the automotive product(s) with -Q.			
PBSS4140T v.2	20050224	Product data sheet	-	PBSS4140T v.1
PBSS4140T v.1	20050214	Product specification	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 1 April 2023