1.5MHz, 1.0A Step-Down Converter

General Description

The LP3220S is a 1.5MHz constant frequency current mode PWM step-down converter. It is ideal for portable equipment which requires very high current up to 1.0A from single-cell Lithium-ion batteries while still achieving over 94% efficiency during peak load conditions. The LP3220S also can run at 100% duty cycle for low dropout operation, extending battery life in portable systems while light load operation provides very low output ripple for noisy sensitive applications.

The LP3220S can supply up to 1.0A output load current from at V_{IN} = 3V and has an input voltage 2.2V to 5.5V and the output voltage can be regulated as low as 0.6V. The high switching frequency minimizes the size of external components while keeping switching losses low. The internal slope compensation setting allows the device to operate with smaller inductor values to optimize size and provide efficient operation.

Order Information

LP3220S - □ □□ □

- F: Pb-Free
- Package Type
  - B5: SOT23-5
- VOUT Type
  - A: Adjustable
  - 12:Vout=1.2V
  - 18:Vout=1.8V
  - 33:Vout=3.3V

Features

- Input Voltage Range: 2.2V to 5.5V
- Output Voltage Range: 0.61V to VIN
- 1.0A Output Current
- High Efficiency: Up to 94%
- 100% Duty Cycle in Dropout
- Low Shutdown Current: <1uA
- 1.5MHz Switching Frequency
- Soft star Function
- Short Circuit Protection
- Thermal Fault Protection
- SOT23-5Package
- RoHS Compliant and 100% Lead (Pb)-Free

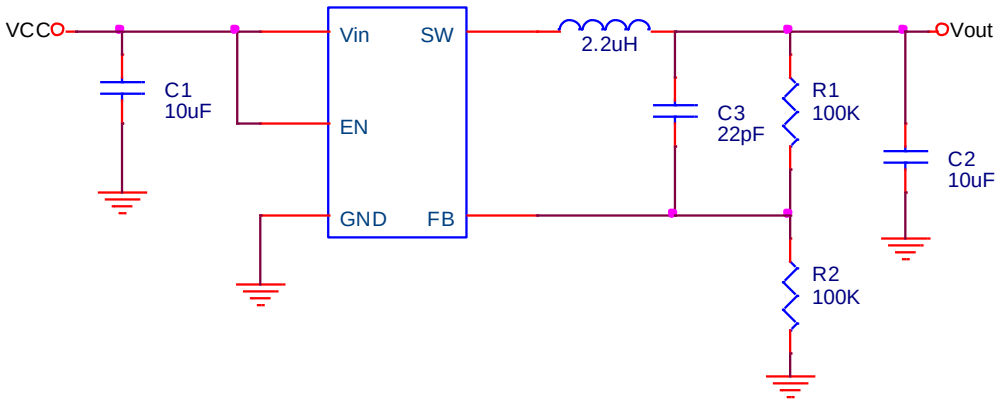
Applications

- Portable Instruments
- DSP Core Supplies
- Cellular Phone and Smart mobile phone
- PDA
- GPS Applications

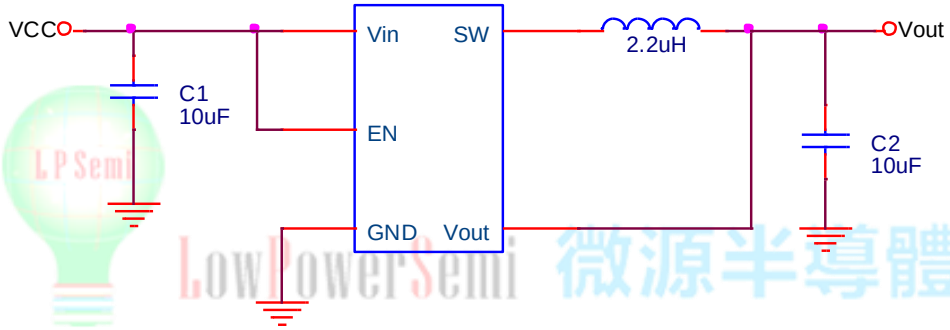
Marking Information

Device	Marking	Package	Shipping
LP3220SAB5F	LPS A1YWX	SOT23-5	3K/REEL
LP3220S-12B5F	LPS AAYWX	SOT23-5	3K/REEL
LP3220S-18B5F	LPS ABYWX	SOT23-5	3K/REEL
LP3220S-33B5F	LPS ACYWX	SOT23-5	3K/REEL
Marking indication: Y:Production year W:Production week X:Production batch			

Typical Application Circuit

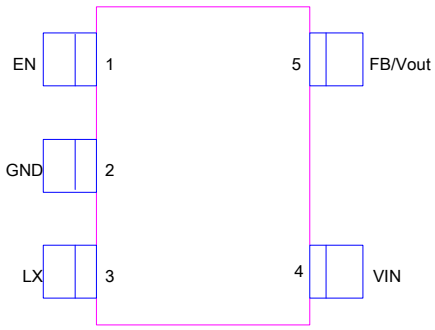


Adjustable Voltage Application Circuit



Fixed Voltage Application Circuit

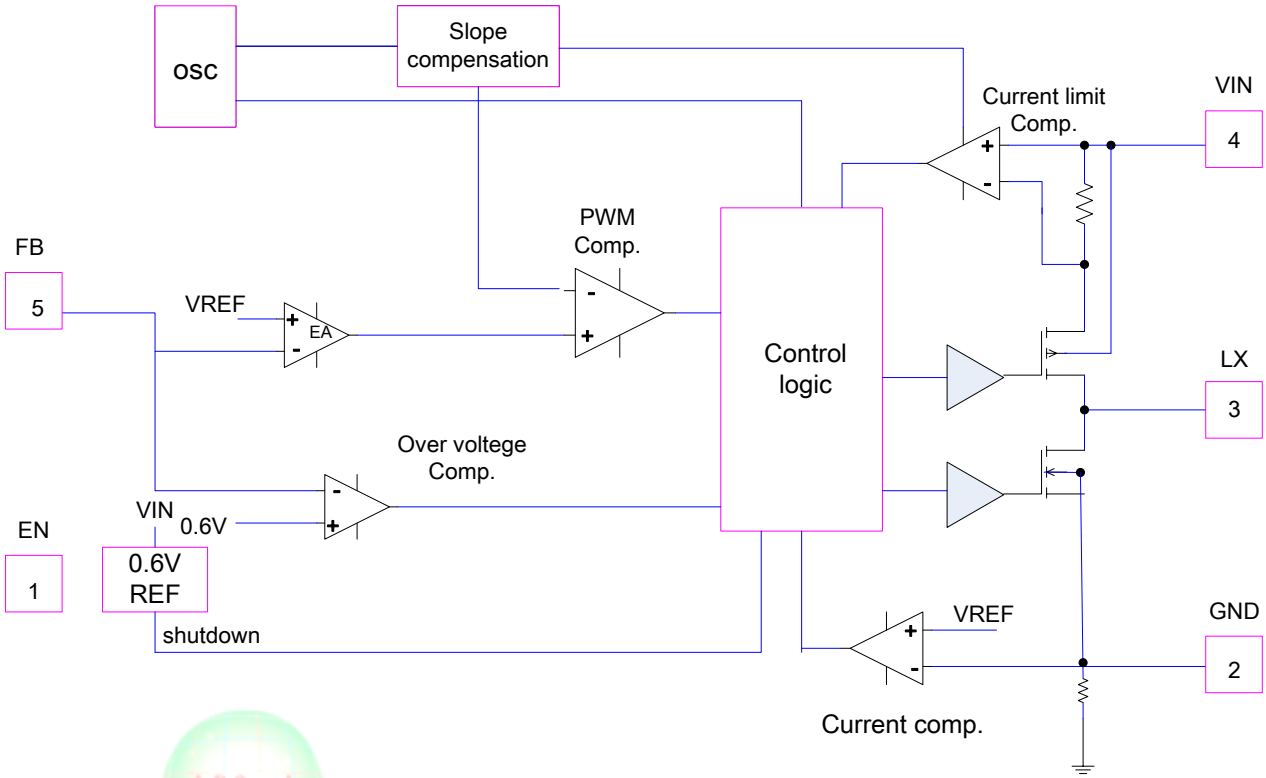
Functional Pin Description

Package Type	Pin Configurations
SOT23-5	Top View 

Pin Description

Pin	Name	Description
1	EN	Enable pin. Active high.
2	GND	Ground.
3	LX	Switch Mode Connection to Inductor. This pin connects to the drains of the internal main and synchronous power MOSFET switches.
4	VIN	Supply Input. Power supply input pin. Must be closely decoupled to AGND with a 10μF or greater ceramic capacitor.
5	FB/Vout	Feedback Input.

Function Diagram



Absolute Maximum Ratings Note 1

- ✧ Input Voltage to GND ---------- 8.0V
- ✧ LX Voltage to GND ---------- 8.0V
- ✧ Output Voltage to GND ---------- 6.0V
- ✧ Other pin Voltage to GND ---------- 6.0V
- ✧ Maximum Junction Temperature ---------- 150℃
- ✧ Operating Ambient Temperature Range (T_A) ---------- -40℃ to 85℃
- ✧ Maximum Soldering Temperature (at leads, 10 sec) ---------- 260℃

Note 1. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Thermal Information

- ✧ Maximum Power Dissipation (SOT23-5, P_D,T_A=25℃) ---------- 0.45W
- ✧ Thermal Resistance (SOT23-5, θ_{JA}) ---------- 250℃/W

ESD Susceptibility

- ✧ HBM(Human Body Mode) ---------- 2KV
- ✧ MM(Machine Mode) ---------- 200V

Electrical Characteristics

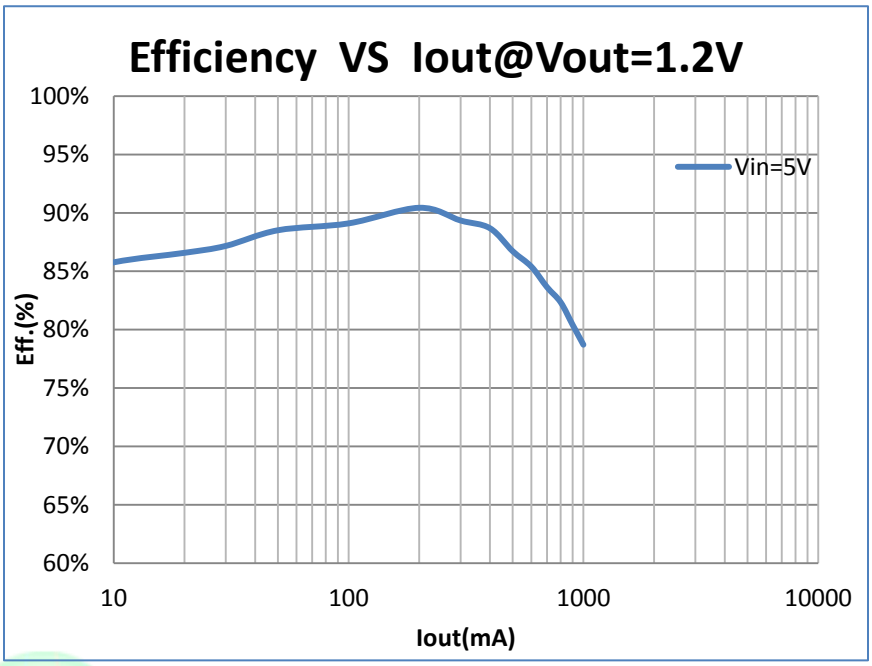
(L=2.2uH, C_{IN}=10uF, C_{OUT}=10uF, V_{IN}=V_{EN}=5V, Typical values are T_A=25℃)

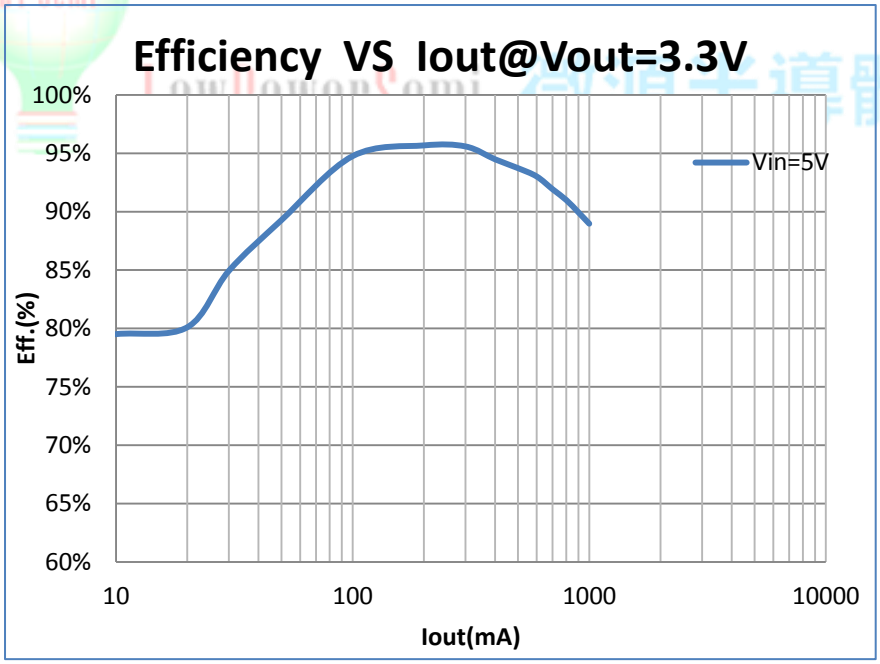
Symbol	Parameter	Condition	Min	Typ	Max	Units
V_{IN}	Input Voltage		2.2		5.5	V
V_{IN_OVP}	Input Over Voltage Protection			6.2		V
V_{IN_OVPHYS}	Input OVP Hysteresis			0.2		V
V_{UVLO}	UVLO Threshold	V_{IN} Rising		2		V
V_{UV-HYS}	UVLO Hysteresis			0.1		V
V_{OUT}	Output Voltage Range	LP3220SAB5F	0.61		V_{IN}	V
		LP3220A-12B5F		1.2		V
		LP3220A-18B5F		1.8		V
		LP3220A-33B5F		3.3		V
I_Q	Quiescent Current			70		μA
I_{SHDN}	Shutdown Current	V_{EN} = GND			1	μA
I_{LIM}	P-Channel Current Limit			2		A
$R_{DS(ON)\_H}$	High-Side Switch On Resistance			300		mΩ
$R_{DS(ON)\_L}$	Low-Side Switch On Resistance			200		mΩ
V_{FB}	Feedback Threshold Voltage Accuracy		0.588	0.6	0.612	V
I_{FB}	FB Leakage Current	V_{FB} = 1.0V		10		nA
F_{OSC}	Oscillator Frequency			1.5		MHz
T_S	Startup Time	From Enable to Output Regulation		110		μs
T_{SD}	Over-Temperature Shutdown Threshold			150		℃
$V_{EN(L)}$	Enable Threshold Low				0.4	V
$V_{EN(H)}$	Enable Threshold High		1.4			V
I_{EN}	Input Low Current	V_{EN} = 5.0V			1	μA

Note: Output Voltage: V_{OUT}=0.6×(1+R_1/R_2) Volts;

Typical Operating Characteristics

(V_{IN}=V_{EN}=5V,L=2.2uH,C_{IN}=10uF,C_{OUT}=10uF,T_A=25℃，unless otherwise noted)





Functional Description

The LP3220S is a high output current switch-mode step-down DC-DC converter. The device operates at a fixed 1.5MHz switching frequency, and uses a slope compensated current mode architecture.

This step-down DC-DC converter can supply up to 1A output current from at V_{IN} = 3V and has an input voltage range from 2.2V to 5.5V. It minimizes external component and optimizes efficiency at the heavy load range. The slope compensation allows the device to remain stable over a wider range of inductor values so that smaller values (2.2μH to 4.7μH) with lower DCR can be used to achieve higher efficiency. Apart from the small bypass input capacitor, only a small L-C filter is required at the output. The device can be programmed with external feedback to any voltage, ranging from 0.61V to near the input volt-age. It uses internal MOSFETs to achieve high efficiency and can generate very low output voltages by using an internal reference of 0.6V. At dropout, the converter duty cycle increases to 100% and the output voltage tracks the input voltage minus the low $R_{DS(ON)}$ drop of the P-channel high-side MOSFET and the inductor DCR.

The internal error amplifier and compensation provides excellent transient response, load and line regulation. Internal soft start eliminates any output voltage over-shoot when the enable or the input voltage is applied.

Current Mode PWM Control

Slope compensated current mode PWM control provides stable switching and cycle-by-cycle current limit for excellent load and line response with protection of the internal main switch (P-channel MOSFET) and synchronous rectifier (N-channel MOSFET). During normal operation, the internal P-channel MOSFET is turned on for a specified time to ramp the inductor current at each rising edge of the internal oscillator, and switched off when the peak inductor current is above the error current. The current comparator limits the peak inductor current. When the main switch is off, the synchronous rectifier turns on immediately and stays on until either the inductor current starts to reverse, as indicated by the current reversal comparator or the beginning of the next clock cycle.

Control Loop

The LP3220S is a peak current mode step-down converter. The current through the P-channel MOSFET (high side) is sensed for current loop control, as well as short circuit and overload protection. A slope compensation signal is added to the sensed current to maintain stability for duty cycles greater than 50%. The peak current mode loop appears as a voltage-programmed current source in parallel with the output capacitor. The output of the voltage error amplifier programs the current mode loop for the necessary peak switch current to force a constant output voltage for all load and line conditions. Internal loop compensation terminates the trans-conductance voltage error amplifier output. The error amplifier reference is fixed at 0.6V.

Soft Start / Enable

Soft start limits the current surge seen at the input and eliminates output voltage overshoot. The enable pin is active high. When pulled low, the enable input (EN) forces the LP3220S into a low-power, non-switching state. The total input current during shutdown is less than 1μA.

Current Limit and Over-Temperature Protection

For overload conditions, the peak input current is limited to 2A. To minimize power dissipation and stresses under current limit and short-circuit conditions, switching is terminated after entering current limit for a series of pulses. The termination lasts for seven consecutive clock cycles after a current limit has been sensed during a series of consecutive clock cycles.

Thermal protection completely disables switching when internal dissipation becomes excessive. The junction over-temperature threshold is 150℃ with 15℃ of hysteresis. Once an over-temperature or over-current fault conditions is removed, the output voltage automatically recovers.

Dropout Operation

When input voltage decreases near the value of the output voltage, the LP3220S allows the main switch to remain on for more than one switching cycle and increases the duty cycle until it reaches 100%. The duty cycle D of a step-down converter is defined as:

$$D = t_{ON} \times f_{OSC} \times 100\% = \frac{V_{OUT}}{V_{IN}} \times 100\%$$

Where t_{ON} is the main switch on time and f_{OSC} is the oscillator frequency. The output voltage then is the input voltage minus the voltage drop across the main switch and the inductor. At low input supply voltage, the $R_{DS(ON)}$ of the P-channel MOSFET increases, and the efficiency of the converter decreases. Caution must be exercised to ensure the heat dissipated does not exceed the maxi-mum junction temperature of the IC.

Setting the Output Voltage

The LP3220S can be externally programmed. Resistors R_1 and R_2 program the output to regulate at a voltage higher than 0.6V. To limit the bias current required for the external feedback resistor string while maintaining good noise immunity, the minimum suggested value for R1 is 50kΩ. Although a larger value will further reduce quiescent current, it will also increase the impedance of the feedback node, making it more sensitive to external noise and interference. The LP3220SAB5F, combined with an external feed forward capacitor (C3), delivers enhanced transient response for extreme pulsed load applications. The addition of the feed forward capacitor typically requires a larger output capacitor C2 for stability. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.6V \times (1 + \frac{R_1}{R_2})$$

$$R_1 = (\frac{V_{OUT}}{0.6V} - 1) \times R_2$$

Inductor Selection

For most designs, the LP3220S operates with inductor values of 1μH to 2.2μH. Low inductance values are physically smaller but require faster switching, which results in some efficiency loss. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where ΔI_L is inductor ripple current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 60% of the maximum load current 1.0A, or

$$\Delta I_L = 600mA$$

For output voltages above 2.0V, when light-load efficiency is important, the minimum recommended inductor is 1μH. Manufacturer's specifications list both the inductor DC current rating, which is a thermal limitation, and the peak current rating, which is determined by the saturation characteristics. The inductor should not show any appreciable saturation under normal load conditions. Some inductors may meet the peak and average current ratings yet result in excessive losses due to a high DCR.

Always consider the losses associated with the DCR and its effect on the total converter efficiency when selecting an inductor. For optimum voltage-positioning load transients, choose an inductor with DC series resistance in the 20mΩ to 100mΩ range. For higher efficiency at heavy loads (above 200mA), or minimal load regulation (but some transient overshoot), the resistance should be kept below 100mΩ. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (1.0A + 300mA).

Output Capacitor Selection

The function of output capacitance is to store energy to attempt to maintain a constant voltage. The energy is stored in the capacitor's electric field due to the voltage applied.

The value of output capacitance is generally selected to limit output voltage ripple to the level required by the specification. Since the ripple current in the output inductor is usually determined by L, V_{OUT} and V_{IN}, the series impedance of the capacitor primarily determines the out-put voltage ripple. The three elements of the capacitor that contribute to its impedance (and output voltage ripple) are equivalent series resistance (ESR), equivalent series inductance (ESL), and capacitance (C). The output voltage droop due to a load transient is dominated by the capacitance of the ceramic output capacitor. During a step increase in load current, the ceramic output capacitor alone supplies the load current until the loop responds. Within several switching cycles, the loop responds and the inductor current increases to match the load current demand.

In many practical designs, to get the required ESR, a capacitor with much more capacitance than is needed must be selected. For continuous or discontinuous inductor current mode operation, the ESR of the C_{OUT} needed to limit the ripple to ΔVO:

$$ESR \le \frac{\Delta V_O}{\Delta I_L}$$

Ripple current flowing through a capacitor's ESR causes power dissipation in the capacitor. This power dissipation causes a temperature increase internal to the capacitor. Excessive temperature can seriously shorten the expected life of a capacitor. Capacitors have rippled current ratings that are dependent on ambient temperature and should not be exceeded. The output capacitor ripple current is the inductor current, IL, minus the output current, Io. The RMS value of the ripple current flowing in the output capacitance (continuous inductor current mode operation) is given by:

$$I_{C\,RMS} = I_O \times \sqrt{\frac{D}{1-D}}$$

ESL can be a problem by causing ringing in the low megahertz region but can be controlled by choosing low ESL capacitors, limiting lead length (PCB and capacitor), and replacing one large device with several smaller ones connected in parallel. In conclusion, in order to meet the requirement of out-put voltage ripple small and regulation loop stability, ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current ratings. The output ripple VOUT is determined by:

$$\Delta V_{OUT} \le \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times f_{OSC} \times L} \times (ESR + \frac{1}{8 \times f_{OSC} \times C_{OUT}})$$

A 22μF ceramic capacitor can satisfy most applications.

Thermal Calculations

There are three types of losses associated with the LP3220S step-down converter: switching losses, conduction losses, and quiescent current losses. Conduction losses are associated with the $R_{DS(ON)}$ characteristics of the power output switching devices. Switching losses are dominated by the gate charge of the power output switching devices.

At full load, assuming continuous conduction mode (CCM), a simplified form of the losses is given by:

$$P_{TOTAL} = \frac{I_O{}^2 \times [R_{DSON(HS)} \times V_O + R_{DSON(LS)} \times (V_{IN} - V_O)]}{V_{IN}} + (t_{SW} \times F \times I_O + I_Q) \times V_{IN}$$

I_Q is the step-down converter quiescent current. The term t_{SW} is used to estimate the full load step-down converter switching losses.

For the condition where the step-down converter is in dropout at 100% duty cycle, the total device dissipation reduces to:

$$P_{TOTAL} = I_O{}^2 \times R_{DSON(HS)} + I_Q \times V_{IN}$$

Since $R_{DS(ON)}$, quiescent current, and switching losses all vary with input voltage, the total losses should be investigated over the complete input voltage range. Given the total losses, the maximum junction temperature can be derived from the θ_{JA} for the SOT23-5 package which is 250℃/W.

Layout Guidance

When laying out the PC board, the following layout guideline should be followed to ensure proper operation of the LP3220S:

1. The power traces, including the GND trace, the LX trace and the IN trace should be kept short, direct and wide to allow large current flow. The L connection to the LX pins should be as short as possible. Use several VIA pads when routing between layers.

2. The feedback trace or OUT pin should be separate from any power trace and connect as closely as possible to the load point. Sensing along a high-current load trace will degrade DC load regulation. If external feedback resistors are used, they should be placed as closely as possible to the FB pin to minimize the length of the high impedance feedback trace.

3. The input capacitor (C_{IN}) should connect as closely as possible to VIN and GND to get good power filtering.

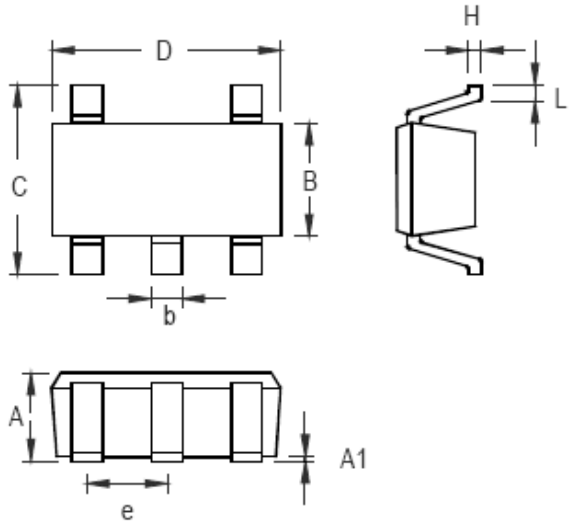
4. Keep the switching node, LX away from the sensitive FB node.

5. The output capacitor C_{OUT} and L should be connected as closely as possible. The connection of L to the LX pin should be as short as possible and there should not be any signal lines under the inductor.

6. The resistance of the trace from the load return to GND should be kept to a minimum. This will help to minimize any error in DC regulation due to differences in the potential of the internal signal ground and the power ground.

Packaging Information

SOT23-5



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.889	1.295	0.035	0.051
A1	0.000	0.152	0.000	0.006
B	1.397	1.803	0.055	0.071
b	0.356	0.559	0.014	0.022
C	2.591	2.997	0.102	0.118
D	2.692	3.099	0.106	0.122
e	0.838	1.041	0.033	0.041
H	0.080	0.254	0.003	0.010
L	0.300	0.610	0.012	0.024

SOT-23-5 Surface Mount Package