International IOR Rectifier

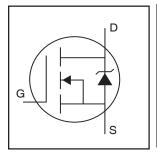
AUTOMOTIVE GRADE

AUIRF2807

HEXFET® Power MOSFET

Features

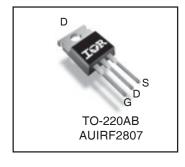
- Advanced Planar Technology
- Low On-Resistance
- Dynamic dV/dT Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified *



_/(_	WIOOI E I
V _{(BR)DSS}	75V
R _{DS(on)} max.	13m Ω
I _{D(Silicon Limited)}	82A®
D (Package Limited)	75A

Description

Specifically designed for Automotive applications, this Stripe Planar design of HEXFET® Power MOSFETs utilizes the latest processing techniques to achieve low on-resistance per silicon area. This benefit combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in Automotive and a wide variety of other applications.



G	D	S
Gate	Drain	Source

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

•	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V (Silicon Limited)	82®	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V (Silicon Limited)	58	А
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V (Package Limited)	75	
I _{DM}	Pulsed Drain Current ①	280	
P _D @T _C = 25°C	Power Dissipation	230	W
	Linear Derating Factor	1.5	W/°C
V _{GS}	Gate-to-Source Voltage	±20	V
E _{AS}	Single Pulse Avalanche Energy ②⑤	340	mJ
I _{AR}	Avalanche Current ①	43	Α
E _{AR}	Repetitive Avalanche Energy ①	23	mJ
dv/dt	Peak Diode Recovery dv/dt ③	5.9	V/ns
T _J	Operating Junction and	EE to . 17E	
T _{STG}	Storage Temperature Range	-55 to + 175	°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)	\neg
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case		0.65	
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50		°C/W
$R_{\theta,JA}$	Junction-to-Ambient		62	

HEXFET® is a registered trademark of International Rectifier.

^{*}Qualification standards can be found at http://www.irf.com/



Static Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	75			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.074		V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance			13	mΩ	V _{GS} = 10V, I _D = 43A ⊕
$V_{GS(th)}$	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
gfs	Forward Transconductance	38			S	$V_{DS} = 50V, I_{D} = 43A$
I _{DSS}	Drain-to-Source Leakage Current			25	μA	$V_{DS} = 75V$, $V_{GS} = 0V$
				250		$V_{DS} = 60V, V_{GS} = 0V, T_{J} = 150^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage			100	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage			-100		V _{GS} = -20V

Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

byttatillo Electrical Characteristics & 1j = 25 o (affices chiefwise specifica)							
Q_g	Total Gate Charge			160		I _D = 43A	
Q_{gs}	Gate-to-Source Charge			29	nC	$V_{DS} = 60V$	
Q_{gd}	Gate-to-Drain ("Miller") Charge			55		V _{GS} = 10V, See Fig.6 and 13 ⊕	
t _{d(on)}	Turn-On Delay Time		13			$V_{DD} = 38V$	
t _r	Rise Time		64			$I_D = 43A$	
t _{d(off)}	Turn-Off Delay Time		49		ns	$R_G = 2.5\Omega$	
t _f	Fall Time		48			V _{GS} = 10V,, See Fig.10 ⊕	
L _D	Internal Drain Inductance		4.5		nH	Between lead, 6mm (0.25in.)	
L _S	Internal Source Inductance		7.5			Between lead, and center of die contact	
C _{iss}	Input Capacitance		3820			V _{GS} = 0V	
C _{oss}	Output Capacitance		610		pF	$V_{DS} = 25V$	
C _{rss}	Reverse Transfer Capacitance		130		[f = 1.0MHz, See Fig.5	

Diode Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions	
I _S	Continuous Source Current			82©		MOSFET symbol	
	(Body Diode)			02@	Α	showing the	
I _{SM}	Pulsed Source Current			280		integral reverse	
	(Body Diode) ①			200		p-n junction diode.	
V_{SD}	Diode Forward Voltage			1.2	V	T _J = 25°C, I _S = 43A, V _{GS} = 0V ⊕	
t _{rr}	Reverse Recovery Time		100	150	ns	$T_J = 25^{\circ}C, I_F = 43A$	
Q _{rr}	Reverse Recovery Charge		410	610	nC	di/dt = 100A/µs ④	
t _{on}	Forward Turn-On Time	Intrinsic	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- ② Starting $T_J = 25$ °C, $L = 370\mu H$, $R_G = 25\Omega$, $I_{AS} = 43A$, $V_{GS} = 10V$ (See Figure 12)
- $\begin{tabular}{l} @ I_{SD} \le 43A, \ di/dt \le 300A/\mu s, \ V_{DD} \le V_{(BR)DSS}, \ T_J \le 175 ^{\circ}C \\ \end{tabular}$
- 4 Pulse width \leq 400 μ s; duty cycle \leq 2%.
- 6 Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A.

Qualification Information[†]

		Automotive (per AEC-Q101) ††					
		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.					
Moisture Sensitivity Level		3L-TO-220 N/A					
	Machine Model Human Body Model		Class M4(+/- 800V) ^{†††} (per AEC-Q101-002)				
ESD			Class H1C(+/- 2000V) ^{†††} (per AEC-Q101-001)				
Charged Device Model		Class C5(+/- 2000V) ^{†††} (per AEC-Q101-005)					
RoHS Compliant		Yes					

[†] Qualification standards can be found at International Rectifier's web site: http://www.irf.com/

^{††} Exceptions (if any) to AEC-Q101 requirements are noted in the qualification report.

^{†††} Highest passing voltage

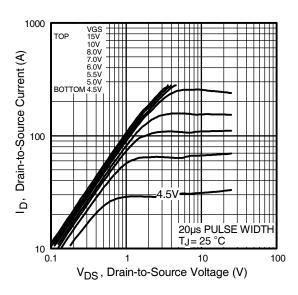


Fig 1. Typical Output Characteristics

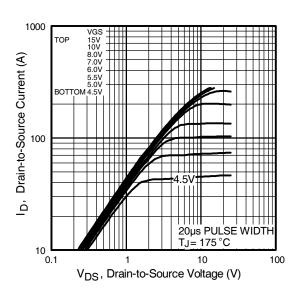


Fig 2. Typical Output Characteristics

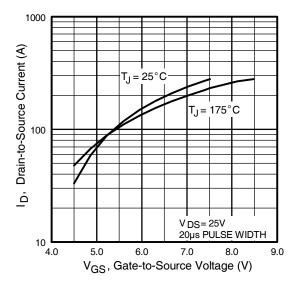


Fig 3. Typical Transfer Characteristics

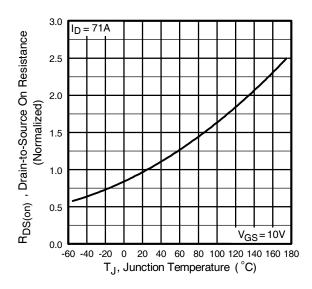


Fig 4. Normalized On-Resistance Vs. Temperature

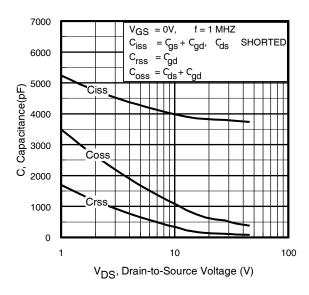


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

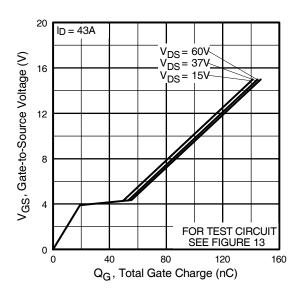


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

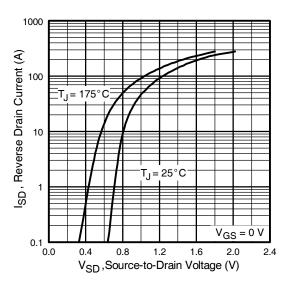


Fig 7. Typical Source-Drain Diode Forward Voltage

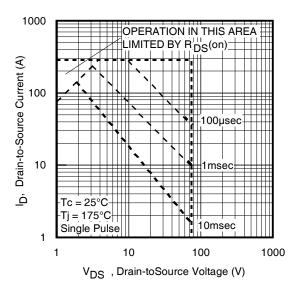


Fig 8. Maximum Safe Operating Area

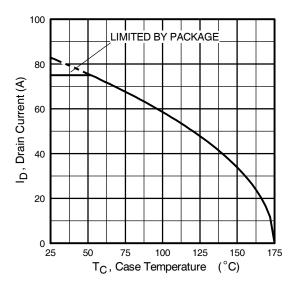


Fig 9. Maximum Drain Current Vs. Case Temperature

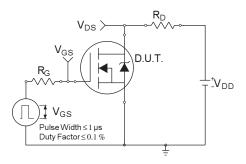


Fig 10a. Switching Time Test Circuit

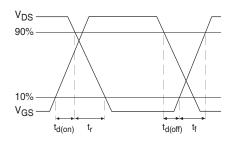


Fig 10b. Switching Time Waveforms

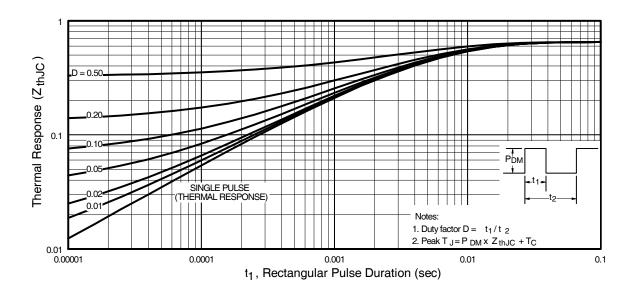


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

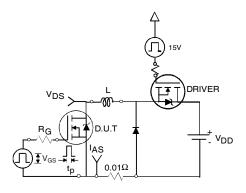


Fig 12a. Unclamped Inductive Test Circuit

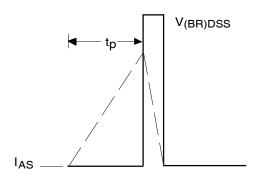


Fig 12b. Unclamped Inductive Waveforms

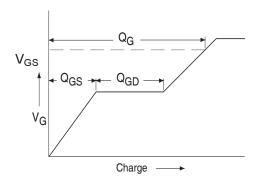


Fig 13a. Basic Gate Charge Waveform

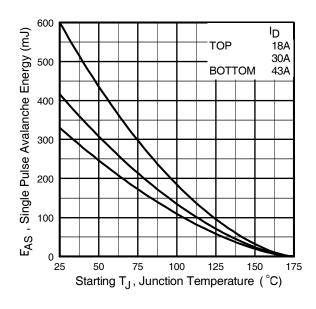


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

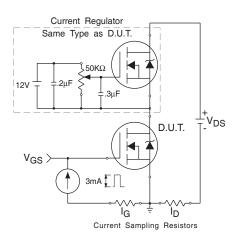
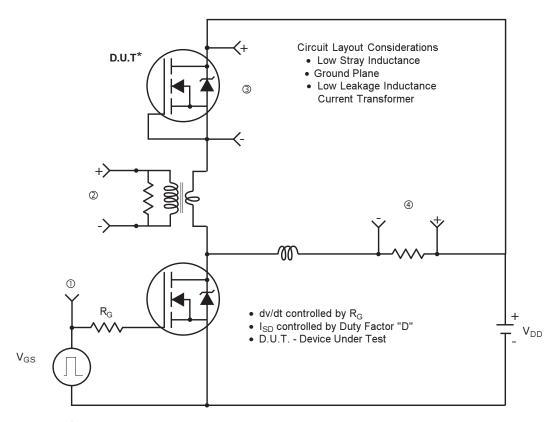
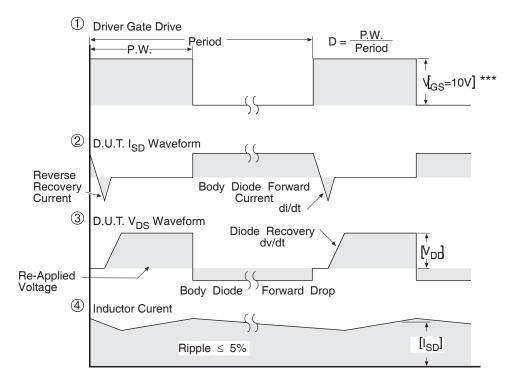


Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit



^{*} Reverse Polarity of D.U.T for P-Channel



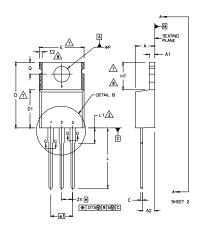
*** V_{GS} = 5.0V for Logic Level and 3V Drive Devices

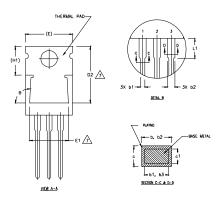
Fig 14. For N-channel HEXFET® power MOSFETs

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TO-220AB Package Outline

Dimensions are shown in millimeters (inches)





NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994. DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS]. LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.

- DIMENSION D & E DO NOT INCLUDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE, THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY,
- DIMENSION 61 & c1 APPLY TO BASE METAL ONLY, CONTROLLING DIMENSION: INCHES,
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E,H1,D2 & E1
- DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES ARE ALLOWED.

	DIMENSIONS					
SYMBOL	MILLIM	ILLIMETERS INCHES		HES		
	MIN.	MAX.		MIN.	MAX.	NOTES
A	3.56	4.82		.140	.190	
A1	0.51	1.40		.020	.055	
A2	2.04	2.92		.080.	,115	
b	0.38	1.01		.015	.040	
b1	0.38	0.96		.015	.038	5
b2	1.15	1.77		.045	.070	
b3	1.15	1.73		.045	.068	
С	0.36	0.61		.014	.024	
c1	0.36	0.56		.014	.022	5
D	14.22	16.51		.560	.650	4
D1	8.38	9.02		.330	.355	
D2	12.19	12,88		.480	.507	7
Ε	9.66	10,66		.380	.420	4,7
E1	8.38	8.89		.330	.350	7
е	2.54	BSC		.100	BSC	
e1	5.	08	 	.200	BSC	
H1	5.85	6,55		.230	.270	7,8
L	12.70	14,73		.500	.580	
L1	-	6.35		-	.250	3
øΡ	3,54	4.08		.139	,161	
Q	2,54	3.42		.100	.135	
ø	90'-	-9.3		90°-	-93°	

LEAD ASSIGNMENTS

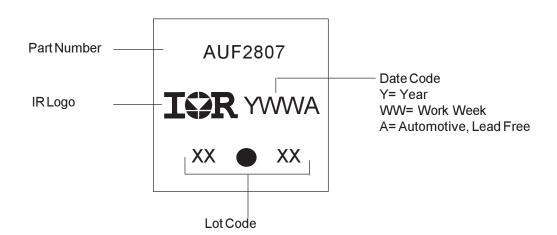
HEXFET

IGBTs, CoPACK

1.- GATE 2.- COLLECTOR 3.- EMITTER

DIODES

TO-220AB Part Marking Information



AUIRF2807

Ordering Information

Base part	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRF2807	TO-220	Tube	50	AUIRF2807

AUIRF2807

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